AMENDMENTS

Please amend the application as follows:

In the Specification:

Please amend Paragraph 27 as follows:

When it is desired to reconfigure the test unit 100, it may be necessary or desirable for the modules 110, 120, $130_{2,1}$ to be separated. In order to unlatch the clips 140, the sequential rotation of clips preferably is followed in reverse order. The clips on the rear module 120 are unlatched first, then the clips on the application module 130_1 next to the rear unit, are unlatched followed by unlatching the clips on the adjacent application unit 130_2. If there are more than two application ~~modes~~ <u>modules</u> in the stack of modules then the additional modules, are similarly sequentially unlatched. The preferred sequential latching and unlatching of the clips has the benefit of reducing the chance that the clips may be unintentionally released or attached. In addition, the sequential latching and unlatching process provides a means for locking modules 110, 120, 130 together with a cable lock 146. When the cable of cable lock 146 is inserted through the eye of tab 148, the clip on the rear module 120 next to the tab can not be rotated from the latched to the unlatched position. Because of the sequential unlatching process, none of the clips in the latching chain can be unlatched.

Please amend Paragraph 33 as follows:

As indicated in the discussion of the FIGS. 1 and 4, the front module has a touchscreen 112 and I/O ports 151, 152, 153, 154 for connecting to external devices. In addition, the front module 110 has a front battery pack 118 and a processor 170. The segmented bus 144 of the test unit 100 preferably is comprised of a protocol bus 172 and a processor bus 174. Each of the application ~~modules 130; preferably provide~~ <u>modules 130 preferably provides</u> a segment of the segmented bus 144. The bus has end segments on the front module 110 and the rear module 120. The control logic 170 preferably is adapted to provide logic and data processing functions for responding to information and data on the protocol bus 172 and the processor bus 174, and for responding to connections to external devices. The front battery pack 118 preferably provides power to the front module 110 when the rear module 120 and application modules 130 are not connected.